

REMARKS

In view of the above amendments and the following remarks, reconsideration and further examination are requested.

By this amendment, claims 27 and 28 have been canceled in favor of new claims 29-31.

Claims 27 and 28 were rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement. This rejection is traversed and is believed to be inapplicable to new claims 29-31.

The Examiner asserted that the present application discloses that the CPU is dependent on another processor or block to activate the CPU, and not “independent” thereof. Thus, the Examiner’s construction of the claims was that the CPU can make a transition without any external input.

However, as has been made clear in the new claims 29-31, the CPU (processor in claim 31) does not always make a transition from the stop state to the operation state due to data being inputted. Therefore, there may be an input without a transition of the CPU to the operation state.

The above claim amendments were discussed during an interview with the Examiner on March 3, 2005. Applicants explained the new claim language and confirmed with the Examiner that the application clearly provides enablement throughout the specification for the recitation that the CPU (processor in claim 31) does not always make a transition from the stop state to the operation state due to data being inputted. The Examiner agreed that the new claims overcome the rejection under 35 U.S.C. § 112, first paragraph.

During the interview, the Examiner also agreed that the new claims appear to be allowable over the prior art Mukai reference as well as the prior art references cited in the Information Disclosure Statement filed concurrently herewith.

In view of the above amendments and remarks, it is submitted that the present application is in condition for allowance. The Examiner is invited to contact the undersigned by telephone to resolve any remaining issues.

Respectfully submitted,

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